

Brief Bio Data of Dr T S Rathore

Prof Rathore holds a PhD degree.

He is an Ex-Prof. of SGSITS Indore (13 years), IIT Bombay (28 years), SFIT Mumbai (8 years), IIT Goa (2 years) and ISTE (2 years),

and Life Senior Member of IEEE, **Life Fellow of IETE**, Life Fellow of IE, Life Member of Computer Society of India, Life Member of Instrument Society of India, Life Member of ISTE,

and author of **Digital Measurement Techniques**, (Narosa, 1st Edition 1996, revised edition 2003, translated in Russian 2004, in Hindi 2020), **Network Analysis** with Van Valkenburg (2019).

He was a Post-Doctoral Fellow at Concordia University (2 Years), Visiting researcher at South Australian University (4 month),

and **Guest Editor** for the special issue on Instrumentation Electronics of Inst of Engrs on Instrumentation Electronics (1992), **Member**, Editorial Board of IETE J of Education (2012-2017), **Member**, Editorial Board of ISTE Indian J of Technical Education, **Regional Editor**, International Journal of Knowledge-Based Engineering Systems (Australia), **Editor-in-Chief** of 8 issues of Sanshodhan Technical Magazine of SFIT, **Chairman** of ISTE IIT-B Chapter (2001-2006),

and **Conference Chair**, IEEE approved International Conference on Circuits, Systems, Communication and Information Technology Applications, CSCITA 2014, **Convener** IEEE (Bombay Section), approved national seminar on 'Circuits, Systems and Signal Processing' 2011, **Convener**, Seminar on 'Mathematical and Computational Techniques in Engineering 2010, **Convener** First SFIT National Seminar INFOCOM 2007, **General Chairman**, IETE Int Conf on Nanotechnology and Health Care Applications (NateHCA) 2007, **General Chairman** of the IETE Int Conf on Quality, Reliability and Control (ICQRC) 2001.

and **Immediate Past Chairman** (2008-2010), **Chairman** (2006-2008), (2001-2002), **Vice-Chairman** (2004-2006), **Honorary Secretary** (1999-2001), **Member** (2003-2004), of the Executive Committee of IETE Mumbai Centre, **Coordinator** for solution manuals of DIPIETE question papers.

and **member** of several expert committees for faculty selections, **questions paper setter** for UPSC, GATE, NTPS, L&T, IETE and several universities, **member** of the board of studies of several universities.

He has **received (selected ones)** (1) **IETE** Hari Ramji Toshniwal Award, (2) IETE K S Krishnan Memorial Award, (3) IETE Prof K Sreenivasan Memorial Award, (4) IETE B R Batra Memorial Award, (5) IETE M N Saha Memorial Award, (6) ISTE U P Government National Award, (7) ISTE Maharashtra State National Award, (8) IEEE Bombay Section Silver Jubilee Medal, conducted/participated several STTP/FDP/summer/winter schools for teachers, delivered invited lectures,

and **Best Paper Awards (all from IETE)**: (1) A systematic method for finding the input impedance of two-terminal networks (2017), (2) NDD functions: Properties and applications (2012), (3) Applications of timer integrated circuits (2010), (4) Design, development & applications of pc-based process control trainer for automation (2008), (5) Programmable gain amplifiers (1995)

Research Contributions: Areas of research: Analysis and Synthesis of Circuits, SC filters, Electronic Circuit Design, Sampling Theory, Electronic Instruments, Electrical Engineering Education. He has published around 265 papers in Journals (IEEE, IEE/IET, Int J of Electronics, Circuit Theory and Applications, Circuits, Systems and Signal Processing, IETE, IE, etc).

Publications: Journals: International: 100 and Indian: 100, Conferences: 42

Exclusively for IETE: (a) Journals 45, (b) Conference: 06

For more details, please visit: <https://www.ee.iitb.ac.in/wiki/faculty/tsrathor>