

3rd International Conference on
Microelectronics, Computing
& Communication Systems
(MCCS-2018)

12-13th May 2018



**Organizer: INSTITUTION OF ELECTRONICS & TELECOMMUNICATION
ENGINEERS & INDIAN SOCIETY FOR VLSI EDUCATION**

Advanced Regional Telecom Training Center

BSNL, Near Jumar River Bridge, Hazaribag Road, Ranchi-835217, Jharkhand, India
Tel: 0651-2273260(BO), Mob. 9973886214(GC), 8877101225(OS), 9431259955(CO)

Web: www.ieteranchi.org, / www.isve.in, E-mail: mccs.2018.02@gmail.com,
admn.iete.ranchi@gmail.com, admn.isve.ranchi@gmail.com

Dear Sir/Madam,

We are glad to inform you that ISVE & IETE Ranchi Centre are organizing two days Zonal Seminar with Zonal ISF Congress: 3rd International Conference on **Microelectronics, Computing & Communication Systems (MCCS-2018)** on 12-13th May-2018 at ARTTC, BSNL near Jumar River Bridge, Hazaribag Road, Ranchi.

Prospective authors are invited to submit full papers describing original, previously unpublished, complete work (not currently under review by another conference/journal) up to six pages in IEEE double column format including figure, results and references. All papers send on conference e-mail: mccs.2018.02@gmail.com, iete.isve.vlsi@gmail.com, admniete.ranchi@gmail.com / admn.isve.ranchi@gmail.com. Papers will be accepted in word format only.

The accepted papers will be published in conference proceedings by Springer with ISBN number and extended versions of papers will be published in SCOPUS, SCI and some other indexed journals. (Name of Journals: 1-IETE Journal of Research (<http://mc.manuscriptcentral.com/tijr>), 2-IETE Technical Review, 3- JOLPE, 4-IETE Journal of Education, 5-WULFENIA, 6-TELEKOMANIKA, 7-Analog Integrated Circuits and Signal Processing, 8- Microsystems Technologies(MST), 9-IJCA , 10- ARPJN Journal etc).

MCCS-2018 will includes oral sessions and poster sessions, tutorials, invited talks, keynote address by renowned Scientists, Professors and Industry persons from India and Abroad on topics related to the conference. Original theoretical, practical, experimental, simulations, development, application, measurement, and testing based papers are invited for presentation in the conference. All submitted papers will go through rigorous plagiarism checking, language checking by expert's reviewers. Details review comments will be send to communicating authors in the form of Accepted, Accepted with changes, and Rejected.

About NCCS-2015/MCCS-2015/NCCS-2016&MCCS-2017:

1st International Conference on **Nano-electronics, Circuits & Communication Systems (NCCS-2015)** has been organized by ISVE & IETE at ARTTC BSNL Ranchi on 9-10th May-2015. A list of peer reviewed and plagiarism free accepted papers have been released & available online in SCOPUS book series LNEE (Lecture Notes on Electrical Engineering)

of Springer : <http://www.isve.in/ContentUploaded/adminisve/file/Final%20Paper%20list%20to%20be%20published%20in%20Proceeding%20of%20ICNCCS%20in%20Springer%20book%20series%20LNEE.pdf> and

see www.springer.com/gp/book/9789811029981 and

extended version of papers have been published by SCI Journals: <http://link.springer.com/article/10.1007/s00542-016-2873-8> and

some other extended version papers are in queue for publications in above listed journals.

Theme of the Conference:

International conference focused on the frontier issues in the Electrical, Electronics, Computer, Communication and Information Technology, Mechatronics, Environmental Science & Engineering, Bioengineering and their applications in business, academic, industry and other allied areas. This international conference main aim to bring together scientists, researchers, engineers from academia and industry. It will provide an international forum to exchange and share their knowledge, experiences, technological developments, and researches in current trends.

Topics of Conference:	
<ul style="list-style-type: none"> • Microelectronics, Circuit & Systems • Nano-electronics Devices • Micro Electro Mechanical System • VLSI Design & IC Technology • IC Fabrication and Testing • CMOS Mixed Signal & RF VLSI Design • VLSI Signal Processing • VLSI for Wireless Communications • VLSI for Bioengineering • VLSI for Instrumentations & Controls • VLSI for Electronic System Design & Manufacturing • Image Processing • Digital Signal Processing • Embedded System • Real Time Embedded System • Robotics, • Electric Power System • Hybrid Vehicles • Renewable Energy • Green Energy • Cloud Computing • Algorithm development and implantation • Computer Networks 	<ul style="list-style-type: none"> • ICT Applications • Computer Architecture • Information Security, knowledge and data mining • Software Engineering & Soft computing • Image Sensing and Processing • Parallel, grid and cloud computing • Mobile Communication and Computing • Ad hoc networks, Pervasive Computing • Wireless Sensor Networks, Body Sensor Network, • Sensor Network Applications in Mines, • Antennae and Diversity • EMI & EMC • Satellite Communications • Fibre Optics Communications and Optical Networks • Quantum Dots • Photonics & Optical Devices • Bioengineering & Telemedicine • RFID & Telemetry Systems • Environmental Science & Engineering, Urban Planning

- Computing systems
- Computer vision

- Mechatronics
- Power Electronics

Books Publications on Pedagogy Methods:

Prospective authors are invited to submit full book chapters describing original, previously unpublished complete work (not currently under review by any conference/others) up to 30 pages including figures, tables and mathematical equations. Main heading font size should be: 16, subheading: 14 & normal texts: 12. All the contents will be accepted in **Time New Roman** only. All books will contain mission and vision. All book chapters will contain: (i) chapter overview, (ii) 5-7 chapter level objectives, (iii) 5-7 chapter units, (iv) 4-5 chapter level problems covering to all chapter objectives. Similarly each unit will contain: (i) 5-7 unit objectives, (ii) 4-5 unit level problems covering to all unit objectives, (iii) unit summary. For reference author can site Washington Acord. All book chapters send on conference e-mail: mccs.2018.02@gmail.com, iete.isve.vlsi@gmail.com. Book chapters will be accepted in word format only. All submitted book chapters will go through rigorous plagiarism checking, language checking by expert's reviewers. Details review comments will be send to communicating authors in the form of accepted, accepted with changes, and rejected.

The accepted book chapters will be published by Springer / other reputed national/ international publishers with ISBN number/ ISSN number.

Name of Proposed Books	
• VLSI Signal Processing	• CMOS Digital VLSI Design with VHDL
• Microelectronics Devices & Circuits	• CMOS Digital VLSI Design with Verilog
• Advanced Semiconductor Devices & Photonics	• VLSI Communication Systems
• Environmental Science & Engineering	• Embedded System & Robotics
• Electrical & Electronics Engineering	• Power Electronics & Control Systems
• IC Design & Technology	• Nano Science & Nano Technology

Please download the book chapters sample copy: [SAMPLE COPY](#)

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This is an Advanced Regional Telecom Training Centre of BSNL. It is 12 Km from Ranchi Railway station and 16 Km from Ranchi Airport. Ranchi is surrounded by hills, waterfalls, forests, and the other places of scenic beauty. Ranchi has a pleasant weather throughout the year. It is well connected by road, rail and air routes with all the major cities of India. The nearest airport and the railway station are in Ranchi. It has regular flight connectivity with New Delhi, Kolkata, Mumbai and Patna.

Registration:

1. Registration fee includes conference kit, breakfast, lunch, dinner and accommodation charges for two days.
2. Please download the **Registration Form** from the mail-attachment/website and send **scan copy of filled Registration Form** with **NEFT receipt** through **e-mail** for early registration. A/C Name: **INDIAN SOCIETY FOR VLSI EDUCATION**, A/C Number: **01670110061831**, IFSC Code: **UCBA0000167**, Bank: **UCO Bank**, Branch: **Mesra Ranchi**, Address: **BIT Main Building Mesra**, Dist: **Ranchi**,

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Categories of Delegates	Before 31th March 2018 Indian Rs	After 31th March 2018 Indian Rs	Overseas USS
Academia	10000/-	11100/-	1000
Students	8000/-	9000/-	900
Corporate Member	12000/-	13000/-	1200

3. 10% discount who will not be avail the accommodation & kit facility/ member of IETE/ISVE. Please inform before the conference & send your membership proof with your form details.
4. For any other participant/spouse other than corresponding author required to pay@3000/- for two days for all conference facility such as accommodation, food etc.

Please download registration form here: **REGISTRATION FORM**

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Please download the program schedule here: **PROGRAM SCHEDULE**

Please download the **SOUVNIER MCCS-2018**

Important Dates:

Conference Schedule	Scheduled Date	
Paper/ Book Chapter Submission Open	25 th Nov. 2017	
Paper/ Book Chapter Submission Deadline	28 th Feb. 2018	
Author Notification	10 th March 2018	
Last Date for Registration	31 st March 2018	
Camera Ready Paper/Book Chapters	15 th April 2018	
Date of Conference	12-13 th May 2018	

Accommodation:

Accommodations will be providing in **BSNL Guest House**, near Jumar River, Hazaribag Road, Ranchi and in some other Hotels in Ranchi for two days. Accommodation details will be communicated to the participants after his/her acceptance of papers and papers registration.

Sponsorship Opportunities:

We welcome sponsorship from companies and organizations who wish to show case their products and services and have the opportunity of networking with academics and practioners in the field. With a truly global audience the conference provides an excellent opportunity for you to show your support for ongoing research and academic excellence by being involved with such important event. Academic institutions may choose to use sponsorship as a vehicle to promote their Masters and Ph.D. programmes. We also welcome sponsorship of specific activities in the conference programme. We believe that sponsoring a conference provides a superb vehicle to showcase your commitment to academic excellence and pursuit of knowledge. Your company will benefit from interaction with a highly focused, motivated, engaged and respective audience in a relaxed environment.

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Banner displayed during conference	12'x5'	8'x5'	6'x4'	3'x2'
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VLSI Design Group

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Microelectronics, Computing & Communication
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