		PROGRAMME OF DIPLET	E EXAI	MINA	ATION
		ELECTRONICS & TELECOMMUN	ICATIO	ON E	NGINEERING
Jun/Dec 15 <sup>TH</sup>	FORENOON 10.00 AM TO 1.00 PM		AFTERNOON		
					2.30 PM TO 5.30 PM
	DE51	Engineering Mathematics - I	DE	59	Electronic Instrumentation & Measurements
	DE68	Television Engineering	+		
16 <sup>TH</sup>	DE52	Fundamentals of Electrical & Electronics Engg.	DE	60	Microprocessors & Microcontrollers
			DE	69	Data Communication & Networks
17 <sup>TH</sup>	DE53	Computer Fundamentals & C Programming	DE	61	Analog Communications
18 <sup>TH</sup>	DE54	Engineering Materials	DE	62	Telecommunication Switching Systems
	DE70	Object Oriented Programming with C++	+		
19 <sup>TH</sup>	DE55	Engineering Mathematics - II	DE	65	Control Engineering
	DE71	Power Electronics			
20 <sup>TH</sup>	DE56	Analog Electronics	DE	63	Digital Communications
21 <sup>ST</sup>	DE57	Networks & Transmission Lines	DE	66	Wireless & Mobile Communications
22 <sup>ND</sup>	DE58	Logic Design	DE	67	Embedded Systems
			DE	99	Communication skills & Technical writing

## Note:

For Lab examinations, Project evaluation - candidates are to contact their centre for the schedule of Lab examination, project evaluation.

- 1. MKS systems of units will be used.
- 2. Candidates to bring their own drawing board/instrument box for Engg. Graphics paper.
- ${\it 3.}\ \ {\it Non Programmable calculators and log tables (mathematical only) may be used.}$