



**THE INSTITUTION OF ELECTRONICS AND TELECOMMUNICATION ENGINEERS (IETE)**  
(Recognized by DSIR, Govt of India)

Prof (Dr) A K Saini, President  
&  
Members of the Governing Council  
of  
The Institution of Electronics and Telecommunication Engineers (India)

Cordially invite you to the

**28<sup>th</sup> Sir C V Raman Memorial Lecture**

on

**“R & D in Electronics for Sashakt Bharat”**

by

Dr P C Panchariya  
Director CSIR-Central Electronics Engineering Research Institute  
(CSIR-CEERI) - Pilani

on

Thursday, 07<sup>th</sup> Mar 2024 at 1600 hrs

at

Delton Hall, IETE  
2, Institutional Area, Lodi Road,  
New Delhi - 110 003

Gp Capt Sonpal Singh (Retd)  
Secretary General

Prof (Dr) A P Thakare  
Chairman, IETE Technical Programmes  
Committee (TPC)

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**Program overleaf**



## Program

**Thursday, 07<sup>th</sup> Mar 2024**

- 1600 hrs      Tea & Networking
- 1630 hrs      Arrival of the Chief Guest and Introduction
- 1635 hrs      Lighting of Lamp and Invocation
- 1640 hrs      Welcome Address
- Prof (Dr) A P Thakare, Chairman TPC
- 1645 hrs      Presidential Address
- Prof (Dr) A K Saini, President IETE
- 1650 hrs      Sir C V Raman Memorial Lecture
- Dr P C Panchariya, CSIR- Central Electronic Research Institute (CSIR-CEERI) Pilani
- 1730 hrs      Presentation of Mementoes
- 1732 hrs      Vote of Thanks
- Gp Capt Sonpal Singh (Retd)
- National Anthem
- 1745 hrs      High Tea